







Introdution



Hot carrier solar cell offers enhanced efficiency by •Slowing carrier thermalisation in the absorber material. •Extract carriers at selective energies so that carrier energy is not lost to the cold contacts.

Energy Selective Contact

Slowing Thermalisation



Black Solid lines represent the CGS bulk phonon dispersion curve; red solid lines represent the CIS bulk phonon dispersion curve. The green, purple, and blue lines are the phonon dispersion curves for a repeating nano-structure of 1 atomic layer of CIS over 2 atomic layer of CGS.

•Phonon engineering

•Hot-phonon bottleneck effect – reabsorption of hot phonons Emission of optical phonons builds up a hot non-equilibrium population. This hot optical phonon population reheats the electron population, thus slowing carrier cooling. •Klemens mechanism – An optical phonon decays into two equal energy acoustic phonons: LTO \rightarrow 2LA(only). •Block Klemens scattering, reduce the thermalisation of the hot carriers. •Improve open circuit voltage, enhance cell efficiency, lower the cost



Confined optic-phonon dispersion curves(solid line) within a superlattice reduced Brillouin zone 0<Q</td>/d, compared with the bulk dispersion curve(dashed \mathcal{T} urve). In the example the layer thickness is 3 lattice units.

Confinement:

- Standing wave, not propagated
- Different

Development of Low Cost CIGS Thin Film Hot Carrier Solar Cells Yige Hu¹, Gijs Bosman¹, and Tim Anderson²

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, big mismatch, not propagated

•Electrical engineering

•Contact was required to \ddot{a}_{10}^{-5} be very thin so that b carriers are collected **5** before they interacted with the lattice.

device design •The should allow collection \mathbf{F} of carriers at an optimal energy range. Carriers having energies outside this range were rejected prevent scattering loss.

•Energy selective \blacksquare contact—quantum

mechanical tunneling is used, only carriers with optimal energy collected ref[3],[4].

•Enhance Auger process to utilize cold carriers

•Bandgap

engineering

•Reduce recombination rate

Simulated J-V curve on the left is of $\boldsymbol{\zeta}$ quantum well device featuring 3nm AlAs barriers and 10nm GaAs well. J-V curve on the right is of 4nm AlAs barriers and 8nm GaAs well.





[1]:T. Dumelow, T.J. Parker, S.R.P. Smith and D.R. Tilley, "Far-infrared spectroscopy of phonons and plasmons in semiconductor superlattices", Surface Science Reports 17 (1993) 151-212. [2]: S. Tamura, D.C. Hurley and J.P. Wolfe, Acoustic-phonon propagation in superlattice", Physical review B 38(2) 1427-1449

[3]: C.Flynn, D. Konig, M.A. Green and G. Conibeer, "Modelling of metal-insulator-semiconductor devices featuring a silicon quantum well", Physica E 42 (9) 2211-2217. [4]: Y. Ando and T. Itoh, "Calculation of transmission tunneling current across arbitrary potential barriers", J. Appl. Phys. 61(4) 1497-1502.

Future work

- simulation results with experimental data
- hot solar cell.



1. Phonon life time experiament. Extract phonon life to validate 2. Find proper Energy Selective Contact material fitting CIGS